From Technologies to Market

PATENTED

MicroLED Displays Intellectual Property Landscape

# SAMPLE



Picture:Vuereal



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- China Star OptoElectronics Technology (CN)
- Mikro-Mesa (TW)
- Sharp/Elux (JP-TW)
- Goertek (CN)
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- Apple Luxvue (US)
- LuxVue Transfer Process Sequence
- Sony (JP)
- Sharp/ELUX (JP) CM
- PSI Corp. (KR)
- Mikro Mesa (TW)
- Goertek (CN)
- BOE Technology (CN)
- CSOT (CN)
- LG Electronics (KR)
- GLO AB (SW)
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- Patent Portfolio Strength Index
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- Previously Nicolas was research assistant at the French research laboratory "CRHEA-CNRS" specialized in GaN technology for microelectronics and optoelectronics. He worked with Soitec on the development of a new generation of GaN-on-Silicon transistor for power and RF applications.
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# **OBJECTIVES AND KEY FEATURES OF THE REPORT**

This report provides a detailed analysis of the microLED display intellectual property landscape: Identify key patents, understand the key technology nodes, identify the key players.

- Time evolution of patents filings and countries of patent filing.
- Which technology nodes are generating the most activity (chip design, transfer, defect management etc.)
- Detailed overview of key patents for each technology nodes
- Which organizations are filing patents in microLED displays?
  - Types of companies (display makers, consumer electronic OEMs, startups, research institutions..)
  - Geographic areas by headquarters
  - Which technology node(s) are they focusing on?
  - Which companies are missing? Why ?
- Detailed analysis of key companies portfolio (Apple, Samsung, LG, BOE, Mikro Mesa, Playnitride, ITRI...)
  - Scope of their portfolio by technology node.
  - Strength, relevance.
- Identify IP collaborations networks between players
- Overview of important "peripheral" patents: technologies non specific to microLED displays but important for the field. For examples:
  - Micro/nano LED wires / columnar LEDs.
  - Mass micro-chip transfer



# **OBJECTIVES AND KEY FEATURES OF THE REPORT**

• The report also provides an extensive <u>Excel database</u> with all patents analyzed in the report.

### • This database allows multi-criteria searches:

Patent information

- Patent publication number
- Hyperlinks to the original documents
- Priority date
- Title
- Abstract
- Patent Assignees
- Segmentation
- Legal status for each member of the patent family



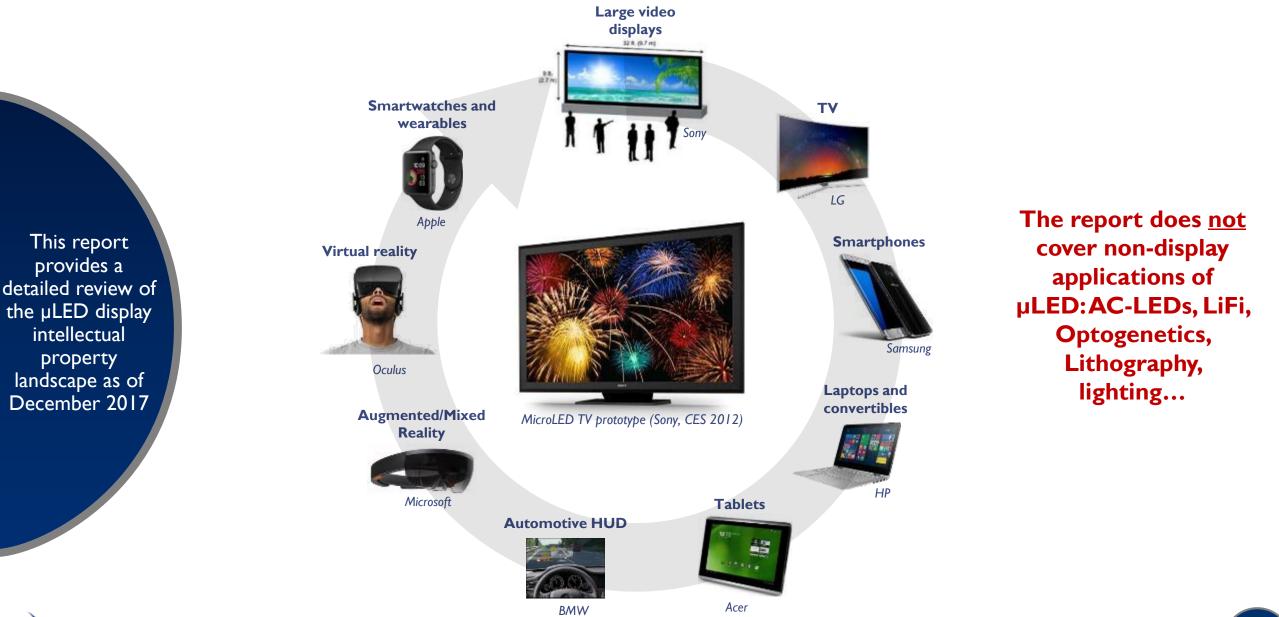
**This report does not provide** any insight analyses or counsel regarding legal aspects or the validity of any individual patent: KnowMade and Yole Developpement are research firms that provide market and technical analysis and opinions. The research, technical analysis and/or work contained herein is not a legal opinion and should not be construed as such.



# SCOPE OF THE REPORT



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property

- The data are extracted from the FamPat worldwide database (Questel-ORBIT) which provides 100+ million patent documents from 95 offices.
- The search for patents was completed in November 2017, hence patents published after this date are not available in this report.
- The patents are grouped by patent family. A patent family is a set of patents filed in multiple countries to protect a single invention by a common inventor(s). A first application is made in one country the priority country and is then extended to other countries.
- The selection of the patents is performed both automatically and manually (next page)

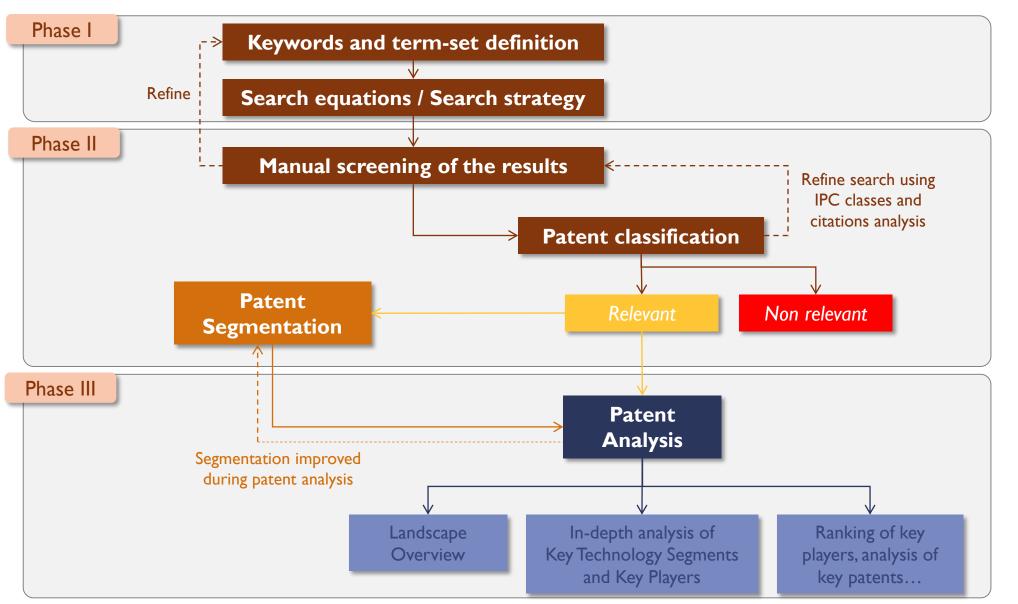
Number of selected patents related to MicroLED displays:

XXXX patents and patent applications grouped in XXXX patent families

- The statistical analysis is performed with Orbit IP Business Intelligence web based patent analysis software from Questel.
- The patents are manually categorized in technical segments by manual analysis of each patent title, abstract, descriptions, illustrations and claims, in conjunction with expert review of the subject-matter of inventions.
- For legal status of European (EP) and PCT (WO) patent applications, EPO Register Plus is used. For legal status of US patents, USPTO PAIR has been used. For legal status of other patents, information is obtained from their respective national registers.



# METHODOLOGY



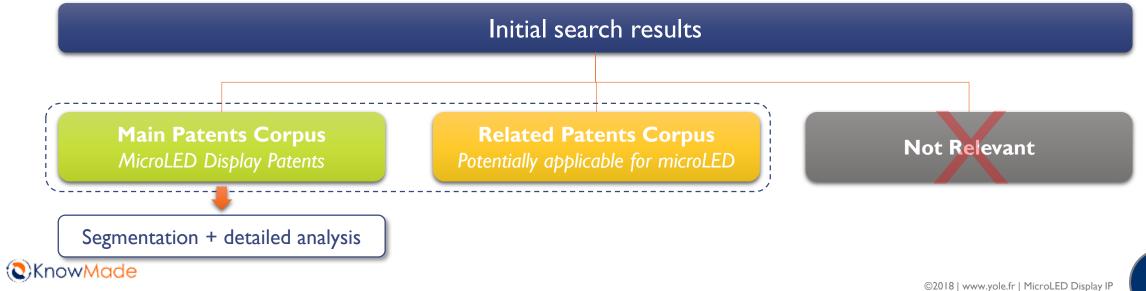


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# PATENT CORPUS

- Large quantities of patents describe technologies that could be used for microLED displays but were not developed specifically for this application. Unless specified, in order to keep the focus on microLED-specific technologies and innovations, those are not included in the main corpus analyzed in this report. However, some of the most relevant are included in the "Related Patents" corpus and discussed separately when pertinent.
- An increasing number of patents include microLEDs among a list of display technologies that can be used in a device that is the object of the invention. For example, patents describing a head mounted device for augmented reality using a microdisplay that could be either OLED, LCD, LCOS or microLED. The invention described in those patents doesn't solve a microLED related issue. Those are therefore excluded from both corpuses (main and related) and not discussed in this report.
- The objective behind this segmentation is to ensure that the main corpus analyzed:
  - I) Accurately represents the increase of activity specifically aimed at solving issues related to the development and manufacturing of microLED displays.
  - 2) Clearly identifies which companies have a strong focus on microLED and which specific problems they are attempting to address.



## PATENT CORPUS

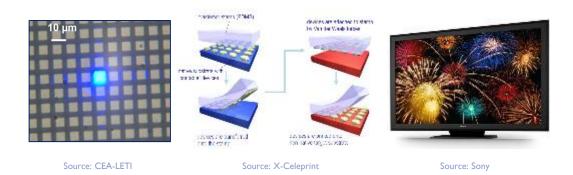
- The main corpus of patents analyzed in this report comprises XXXX patents grouped in XXXX families that focus on inventions explicitly related to the use of microLED in display applications.
- Our selection of "related patents" comprises 55 families and includes patent that don't explicitly mention microLED displays but are considered pertinent to the field. Those are not included in the analysis (graphs and tables) but are discussed individually on a per needed basis. More details and examples are given in the following pages.

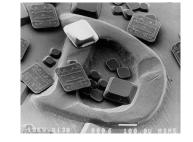
### "Main Patents" Corpus XXXX Patents / XXXX Families

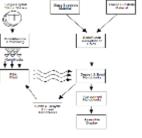
Patents explicitly related to microsized LEDs (<  $50x50 \ \mu m^2$ ) for display applications: manufacturing, assembly, testing, defect management, microled display architecture and driving, color conversion and management etc.

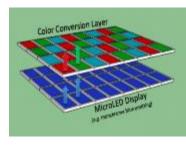
### "Related Patents" Corpus XXXX Patents / XX Families

Patents describing technologies non specific but potentially highly pertinent to microLED display manufacturing: microchip massively parallel transfer and assembly, high resolution color conversion schemes, "mini-LED" (<100 x 100  $\mu$ m<sup>2</sup>) etc.









Source: Alien Technology

Source: Alien Technology

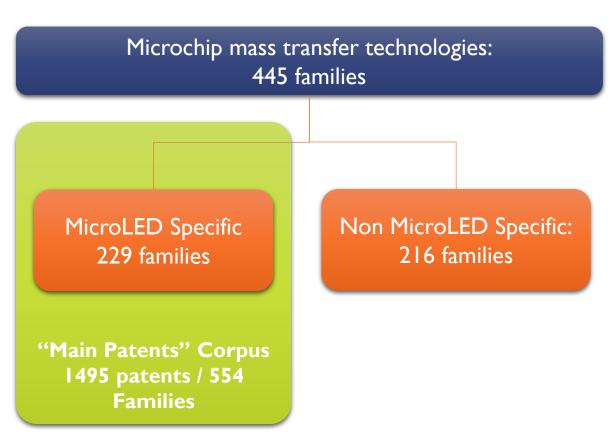
Source: Verlase



# ADDITIONAL RESEARCHES

- Due to the importance of mass transfer technologies for microLED displays a dedicated search for this topic was also conducted. It resulted in a corpus of XXX patent families organized in 2 groups:
  - XXX families discussing mass transfer specifically for microLED (included in the "Main Patent" corpus)
  - XXX families non specific to microLED
- Similarly, we conducted a search on nano/micro wire LEDs.

We have organized the patents in 2 corpuses ("Relevant" and "Related") and conducted additional searches on 2 groups of technologies: microchip mass transfer and nanowires.

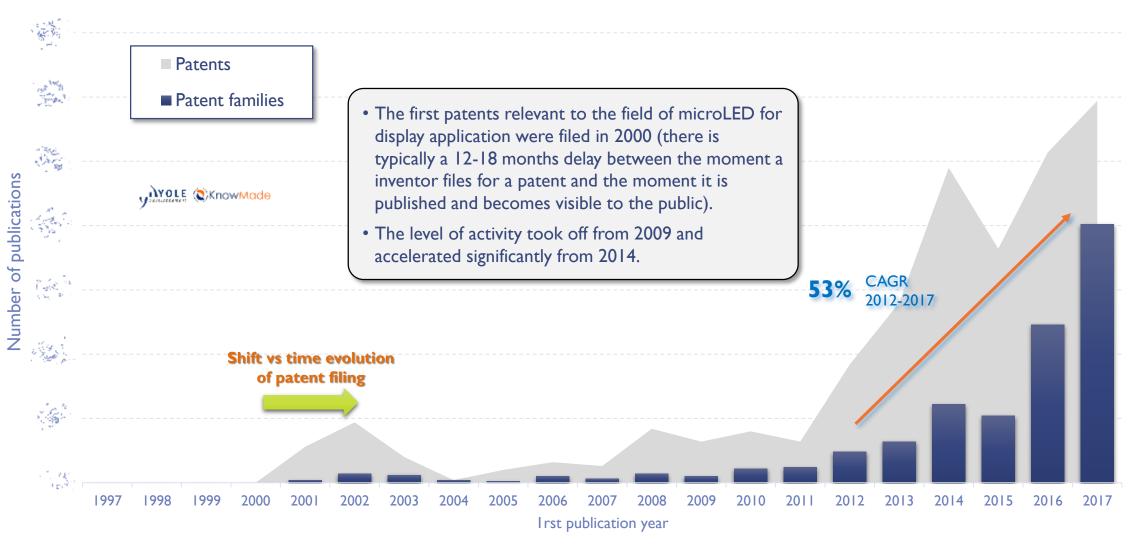




# TIME EVOLUTION OF PATENT PUBLICATIONS

### Time evolution of patent publications

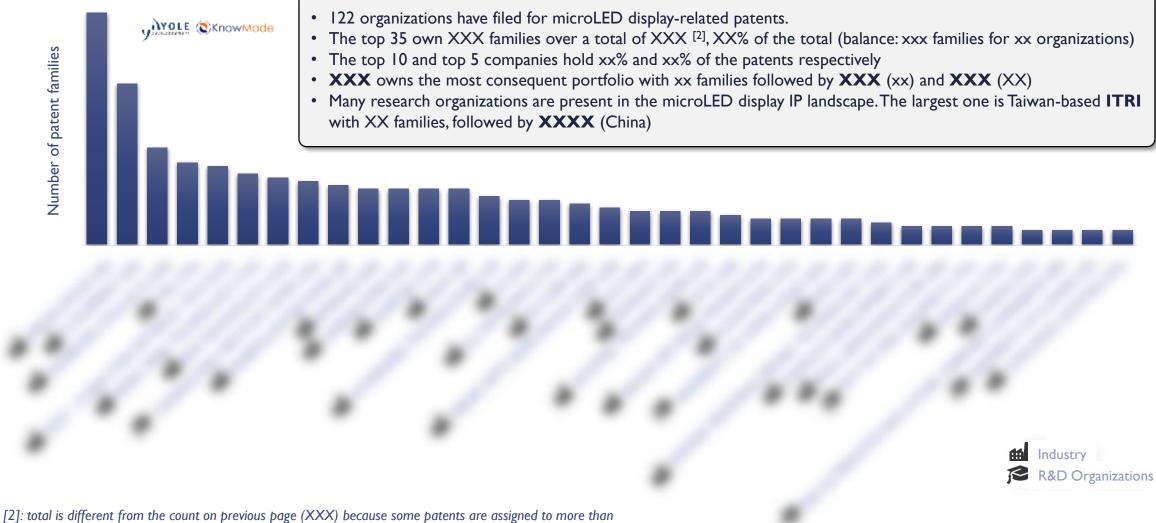
XXXX patents (XXX patent families\*), including XXX granted patents and XXX pending patent applications



\* A patent family is a set of patents filed in multiple countries by a common inventor(s) to protect a single invention.

# LEADING PATENT APPLICANTS

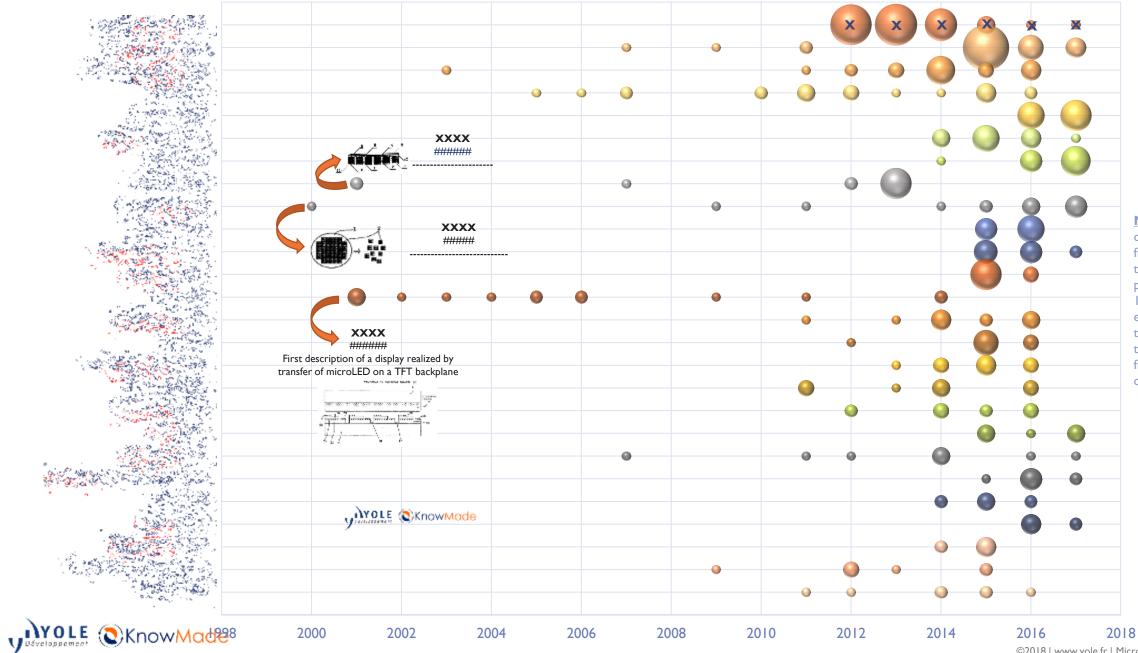
### Ranking of patent assignees according to the number of their patent families



one company and therefore counted twice for this graph.



# TIME EVOLUTION OF PATENT APPLICATIONS



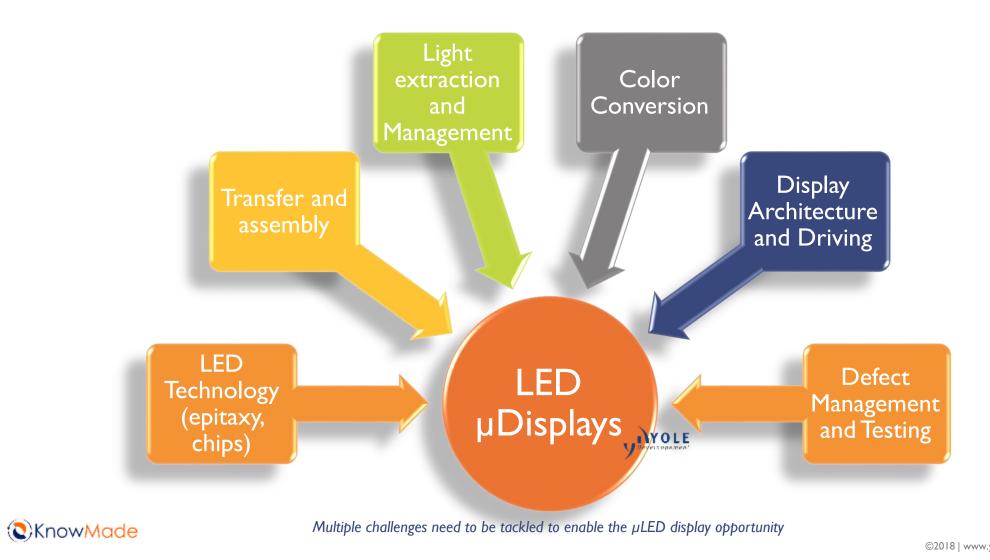
**Note**: Due to the delay between the filing of patents and the publications by patent offices, usually 12-18 months, we estimate that most of the 2017 filings and up to half of the 2016 filing are not visible as of December 2017.

# POSITIONING OF ESTABLISHED PANEL MAKERS

	Korea	Taiwan	China	Japan
Background	Dominates OLED technology and manufacturing	Strong position in LCD challenged by the rise of Chinese makers that could trigger overcapacity and	Growing fast. Soon-to-be leader on LCD	Former LCD display leader. Now fallen behind on capacity and market share.
Buchground	(LG in TV, Samsung in smartphones)	price wars. Some OLED technology but no manufacturing capacity	price, capacity and shipping volumes.	No OLED capacity but some technology (Sony, JOLED, Sharp)
Current Strategy	Phasing out <g8 fabs<br="" lcd="">Massively expanding OLED capacity (up to \$24b combined over the next 3-5 years).</g8>	No major investment plans. Trying to maintain margins through product mix optimization, yield improvements and high utilization rates. Foxconn might invest in LCD through Sharp (acquired in 2016). Position regarding OLED unclear.	Massive investments in next generation LCD (G10.5). + Massive investment on flexible OLED for smartphone. But technology and manufacturing expertise is ~3-5 years behind Korea	No major investment plan beside Sharp/Foxconn possible new LCD fabs in China and the US. OLED possibly a card to play with JOLED: could be the first solution-printed technology ready for market. But access to capital will be challenging
Interest for microLED	Defensive	Offensive: Chance to level playing field for next generation display	Offensive: Chance to level playing field for next generation display	Offensive: Trying to regain former glory with disruptive technology
	de			©2018   www.yole.fr   MicroLED Display IP

# MICROLED DISPLAY MANUFACTURING CHALLENGES

While very promising in terms of performance, there are still multiple performance and manufacturing challenges that need to be addressed to enable cost effective, high volume manufacturing of high performance µLED Displays. The technology segmentation used to analyze the corpus of patents selected for this report follows the major nodes identified in this breakdown



### **TECHNOLOGY SEGMENTATION**

### "Main Patents" Corpus 1495 patents / 554 Families

### Front End

### Epitaxy

Substrates, epitaxy process or equipment specifically aimed at improving the performance, yield, homogeneity of microLED and/or enabling the realization of LED emitters with different wavelength (e.g.: R, G, B) on the same wafer.

### Chip design and

### manufacturing

Structure or manufacturing processes to improve efficiency or other performance of the LED emitter. E.g.: etching techniques to reduce sidewall defects, current confinement layers, aperture to reduce non-radiative surface recombination and improve efficiency of microLEDs, addition of sacrificial layers to reduce kerf losses etc.

# Transfer and interconnect

Techniques for microLED chip separation (release layers and mechanisms etc.), die stabilization, transfer from a donor substrate to a backplane and methods to realize the bonding and electrical connections of the chips with the backplane or the PCB.

### **Monolithic Structures**

MicroLED microdisplay structures where full arrays of microLED are assembled directly (hybridization or direct growth) with the backplane (typically Si-CMOS)

# Light extraction and management

Process, structures or components for light extraction, beam shaping, optical cross talk management. Can involve display structure (e.g.: pixel patterns, reflective layers in pixel cavity, black matrix etc.) or external optics as long as they are part of the display structure (e.g.: microlens array built or bonded directly onto the display stack)

### Color Generation, Conversion, Management

Techniques for color conversion via phosphor, quantum dots or other downconverters. Also color filtering and techniques allowing to create R,G,B pixels from the same substrate (the latter also classified in the "Epitaxy" category.

### **Back End**

### Pixel / display architecture, driving

Describes pixel and/or of full display architecture: pixel/subpixel arrangements, pixel cavity, pixel interconnection schemes, electrode configurations, pixel driving etc.). Patents in this category often discuss the architecture but not the method to assemble and manufacture the display (e.g: transfer process, assembly etc.)

### **Defect Management**

Discuss techniques specifically aimed at mitigating or reducing display defects. improving repairability or the repair technique itself. For example: pixel redundancy, electrode arrangement to easily disconnect defective pixels, laser repair strategies etc.

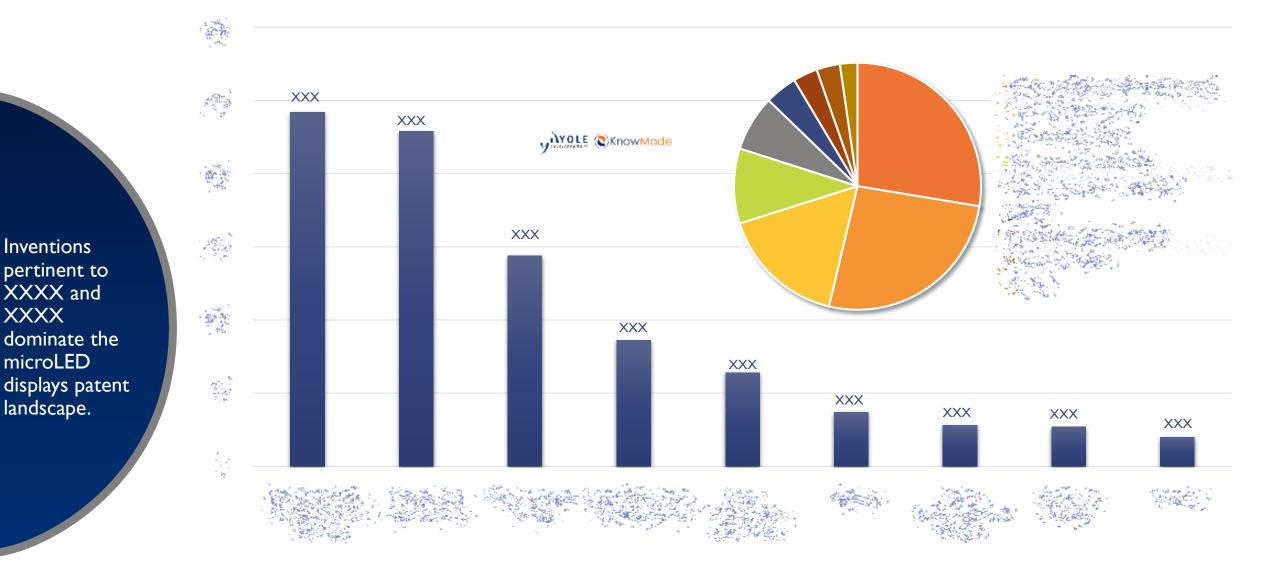
### Testing

Techniques or tools aimed at testing individual pixels before or after assembly or testing a full display. Electrical or optical, contact or contactless.

### Sensors In Display

Describes the integration of sensing or other functions into microLED the frontplane (sensors, pixel drivers positioned between LED emitters). in Example: touch, gesture IR recognition, imaging (fingerprint, iris, facial recognition)

# **OVERVIEW OF PATENT FAMILIES PER TECHNOLOGY NODE**



Note: a patent can belong to multiple categories. For example, a specific chip structure can be aimed at enabling its transfer (e.g. sacrificial layers), improving light extraction (mirrors) etc. Similarly, a transfer and assembly technology can be designed to enable pixel repair and defect management etc.



Inventions

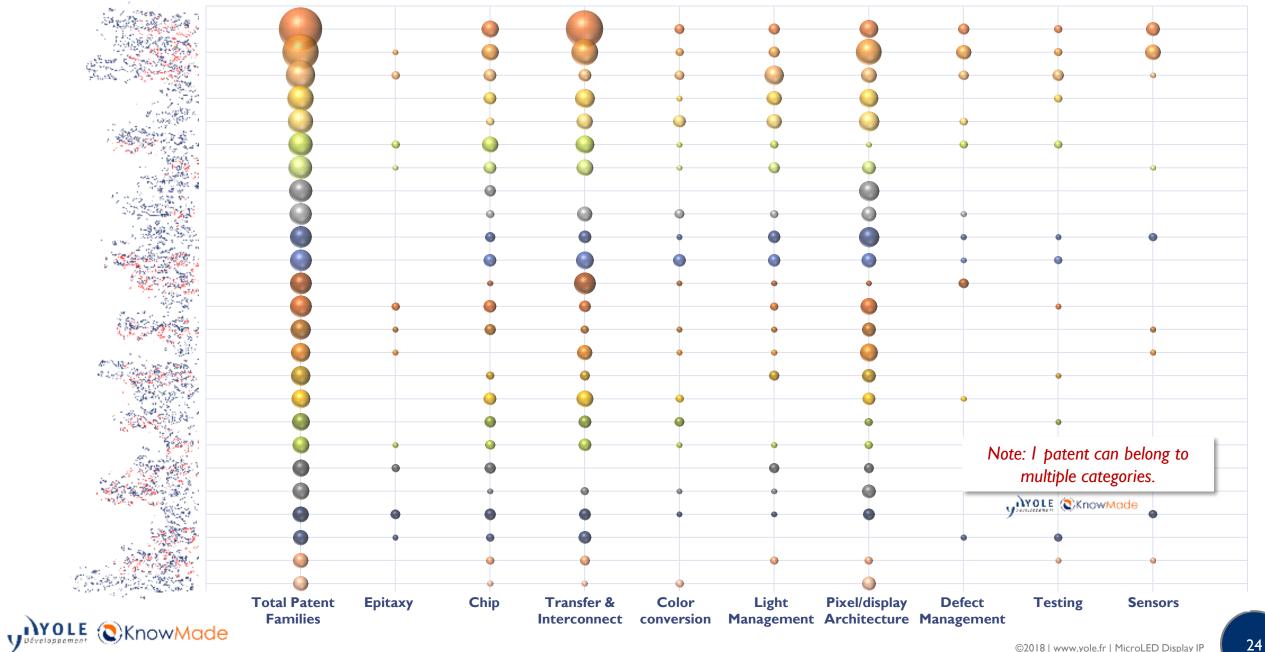
XXXX and

microLED

landscape.

XXXX

# PORTFOLIO TECHNOLOGY SEGMENTATION PER COMPANY (TOP 25)



# BREAKDOWN BY COMPANY TYPES

- We have segmented the patent corpus by various types of companies. The different categories are presented below.
- Some companies could arguably belong to different or multiple categories. For example, we chose to include Sony in the "Display Makers" group although it had stopped producing its own panels and also sells various consumer electronic goods beyond just TV or monitors.



Include various component makers (drivers, PCB, semiconductors) as well as individuals who have filed patents under their own name and have no other assignees.



# TIME EVOLUTION OF PATENT APPLICATIONS PER COMPANY TYPES

WOLE KnowMade 2004 2005 2006 2007 2008 2009 2010 2011 2012 2013 2014 2015 2016 2017 2018 1999 2000 2001 2002 2003

Time Evolution Of Patent Applications Per company Types



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**Note**: Due to the delay between the filing of patents and the publications by patent offices, usually 12-18 months, we estimate that most of the 2017 filings and up to half of the 2016 filing are not visible as of December 2017.

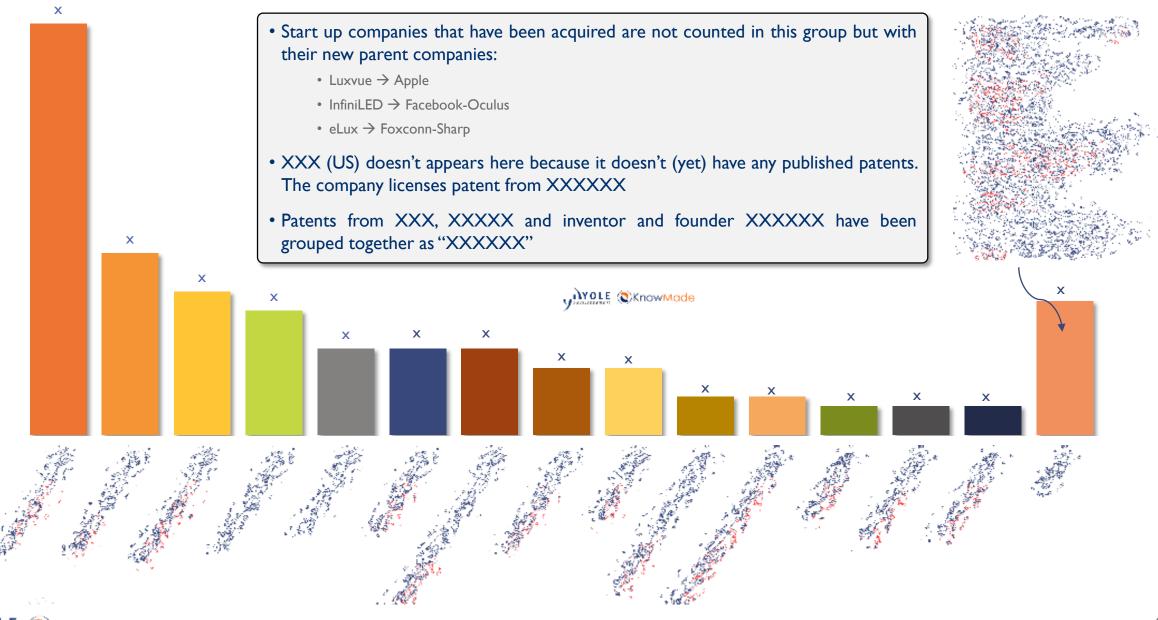
# MOST ACTIVE COMPANIES BY TYPE

 $\mathbf{x}$  = number of patent families

Start Up	OEM/CE	Display Make	ers	R&D Organizatior	ns	LED Ma	.kers	Others	
XXX XX	State xx	Selatar (	xx	5. m	xx	este:	xx	1000	xx
	K ne xx	19.34 (2	xx	162.75	xx	the second	xx	the is	xx
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX	xx XX	A 4 3 5 5 . (2	xx	A Standar	xx	T. A.L.	xx	1000	xx
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX	xx	10 m 10 m	xx	Ser Car	xx	Sec. A.S.	xx	at the second	xx
sign, xx	and the way		xx	(Tres	xx				
XXX XX	XX		xx	-	xx				
XXX XX			xx		xx				
xx			xx	R.S.	xx				
xx				123	xx				



# START UP COMPANIES





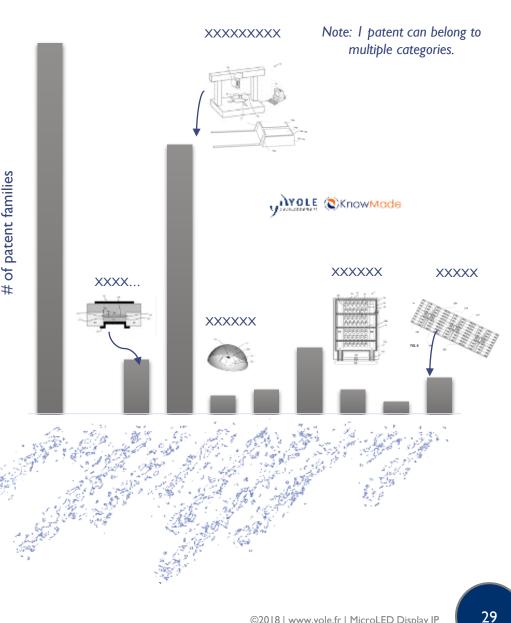
**APPLE- LUXVUE (US)** 

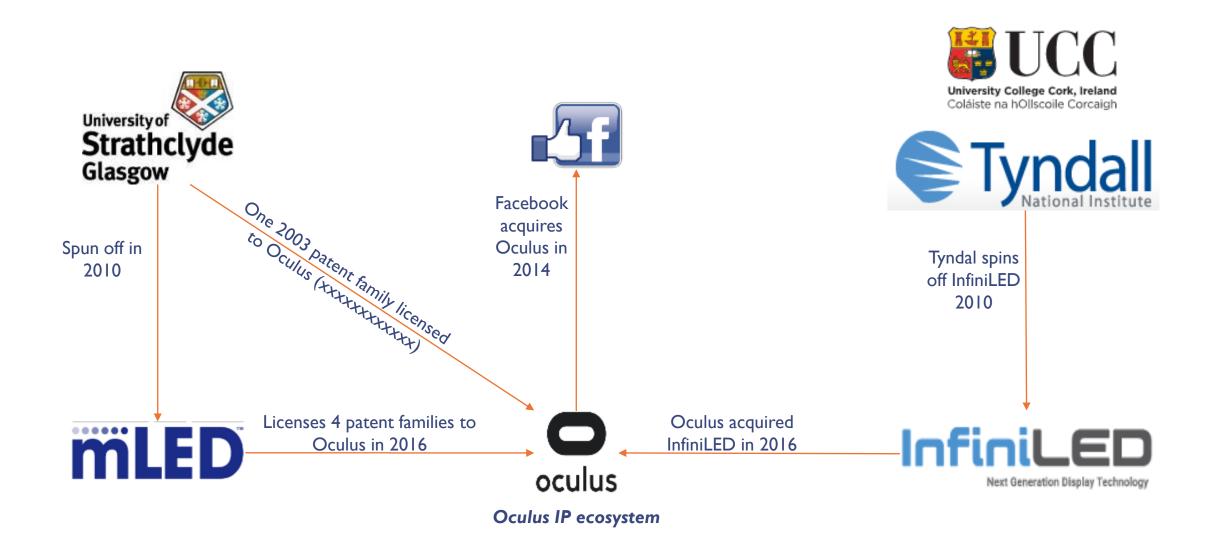
NowMade



Luxvue was established in 2009 to leverage on technologies on electrostatic MEMS microtransfer technologies from FujiFilm Dimatix to develop microLED display

- Luxvue was established in 2009 and raised at least \$43M • from various investors including XXX prior its acquisition by Apple in 2014.
- The first patents were filed in XX. A total of XXX • applications with broad geographic coverage have been filed (XXX families). XXX have been granted, 3 rejected and XX abandoned. The balance is pending as of Dec. 2017.
- Many patent families have already received large numbers of citations from other applicants.
- All this indicate an overall strong IP position in the field • with many seminal patents <sup>[1]</sup> and possibly significant blocking potential in various of the technology nodes (see next section of the report).
- However, while the portfolio covers a broad scope of technology nodes, patents often focus on the technological ecosystem developed around the company's MEMS transfer technology. The blocking potential might therefore be more limited for transfer technologies that differ enough from that of Apple/Luxvue.







# COLLABORATION NETWORK

Number of microLED display patent families

Number of co-assigned patent families

Co-assigned patents revel

collaborations

companies and

organizations.

include XXXX collaboration with XXXX and XXX

with XXXX.

Notable exception

mostly

between

research





### XXXXXXXXXXXXXXXXXXX

XXXXXX (patent granted in USA and Korea, and currently pending application in Europe). Patent rights acquired by XXXX from XXXX in 2017.

XXXXXXXXXXXX

Patent co-filed by XXXXXXX and XXX in 2007. XXXXXX (patents issued in USA, Japan, Taiwan, China, Korea and Malaysia)

Patent rights acquired by XXXXX from XXXXXXXXXXX in March 2017.

XXXXX

Patent co-filed by XXXX and XXX n 2015. International application XXXX and European application XXXX currently pending.

### XXXXX

XXX

Patent co-filed by XXXX and XXXX in 2016. International application and European XXXXX application currently pending. XXXX's XXXXX granted in 2017.

XXXXXXXXXXXXXXXXX Patent co-filed by XXXXXXXXXXXX and XXXXXXXXXXXX in 2015. International application XXXXXXXX currently pending.

XXXX changes name to XXXX. On XXXX, XXXX has been sold to a Chinese consortium consisting of the investor 

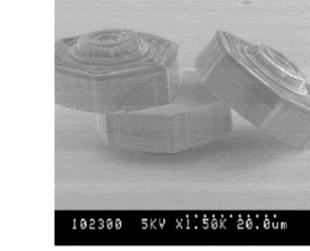
WOLE KnowMade

### XXXXXXXX Patent co-filed by the XXXX and XXXXXXXXXX in 2014. European application XXXXXXX currently pending.

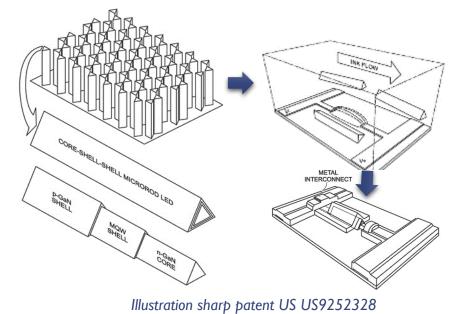


# FLUIDIC SELF ASSEMBLY

- In fluidic self assembly processes, the microLED die are dispersed in a fluid which then flows onto the surface of the substrate where the emitters have to be assembled.
- Self assembly is usually ensured by giving the die specific shapes and etching small cavities (AKA "wells") with matching shapes on the receiving substrate.
  - **XXXX** developed triangular, hexagonal pillar or disk shaped  $\mu\text{LED}$
  - **XXXX** has various patents with disk shape emitters fitted in a concave shape well.
- Alternatively or in addition, the process can be assisted by electrostatic force or electromagnetic polarization.
- Some companies focus on making the process deterministic enough so that each cavity is filled with the corresponding LED emitter.
- Other companies plan for multiple redundancies with subpixels containing multiple emitters to ensure that a sufficient number of microLED chips will populate the cavity and guaranty that each subpixel can light up.
  - **XXXX** in Korea have proposed various strategies leading to semi-randomly organized LED arrays.



microLED build for geld/fluidic transfer (source: Nth Degree)



µLED are dispersed in a liquid binder

(\)KnowMade

In fluidic

assembly, the

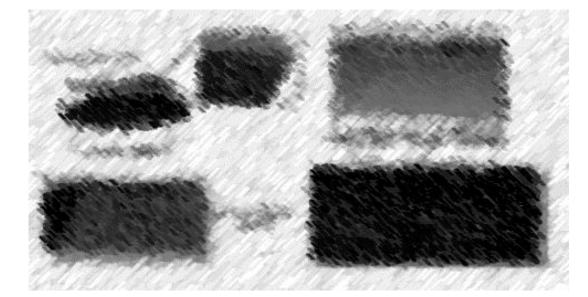
©2018 | www.yole.fr | MicroLED Display IP



# RANKING BY STRENGTH INDEX OF THE PATENT PORTFOLIO



- XXX ranks first. The organization holds a small of number of patent families but gets a lot of citations. The funding patent family for their technology represented for example by XXXX filed in 2007 is co-assigned to XXXXX.
- The two organizations together therefore have a strong position in the field and a long history in developing transfer technologies based on XXXXX:

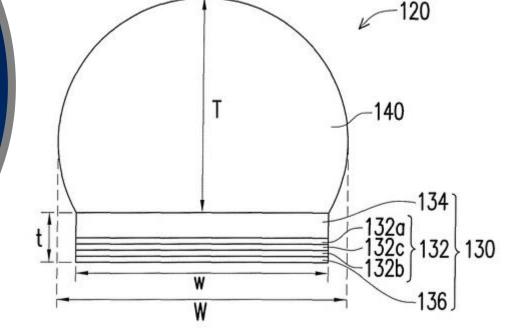


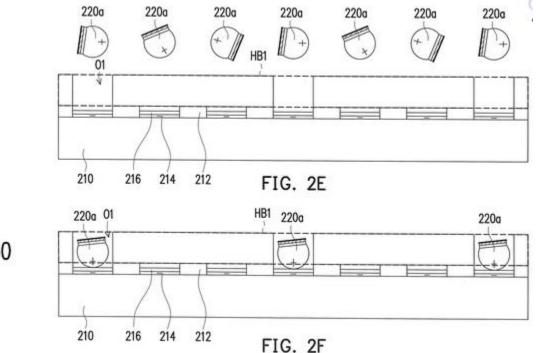
# Strength index of the patent portfolio NOLE KnowMade



# INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE

ITRI has developed at least 3 different transfer technologies







# X-CELEPRINT (UK)- UNIVERSITY OF ILLINOIS (US) - WO2008143635

### **Optical systems fabricated by printing-based assembly**

As shown, 100 pixels, ~11' display,

### Abstract

The present invention provides optical devices and systems fabricated, at least in part, via printing-based assembly and integration of device components Optical systems of the present invention comprise semiconductor elements assembled, organized and/or integrated with other device components via printing techniques that exhibit performance characteristics and functionality comparable to 10 single crystalline semiconductor based devices fabricated using conventional high temperature processing methods Optical systems of the present invention have device geometries and configurations, such as form factors, component densities, and component positions, accessed by printing that provide a range of useful device functionalities.

### Ist claim:

A method of making a semiconductor-based optical system, said method comprising the steps of:

providing a device substrate having a receiving surface; and

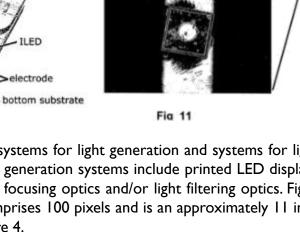
assembling a printable semiconductor element on said receiving surface of said substrate via contact printing; wherein said printable semiconductor element comprises a semiconductor structure having a length selected from the range of

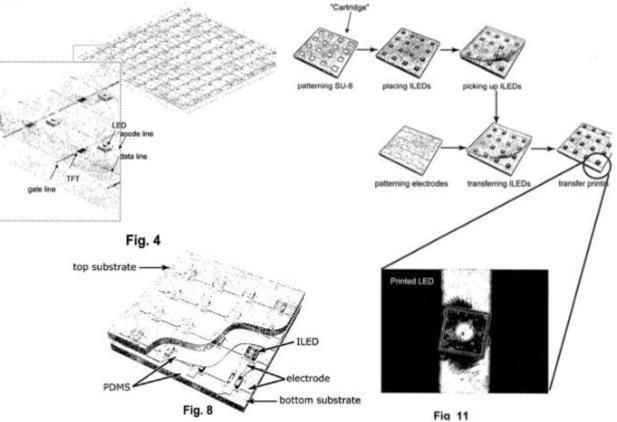
0.0001 millimeters to 1000 millimeters, a width selected from the range of 0.0001 millimeters to 1000 millimeters and a thickness selected from the range of 0.00001 millimeters to 3 millimeters.

### **Description:**

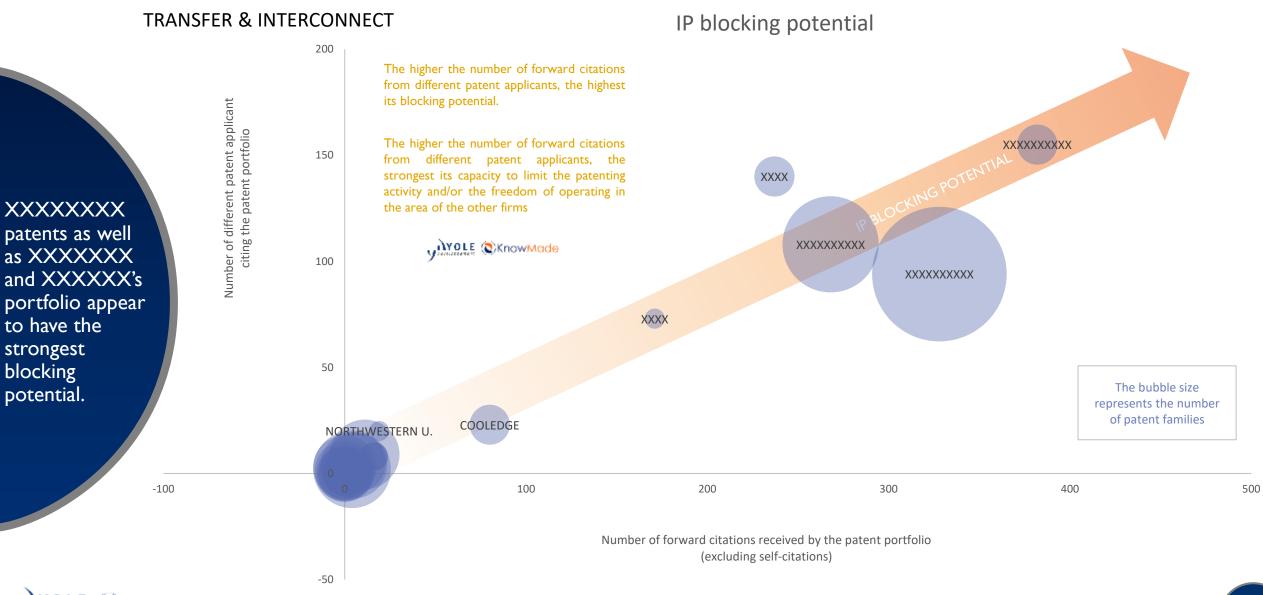
The present invention provides a number of classes of optical systems and related methods of making these systems, including systems for light generation and systems for light harvesting, comprising printed inorganic optical and optoelectronic systems with integrated optical components in registry. Light generation systems include printed LED displays, micro LED devices, passive matrix LED displays, active matrix LED displays [...] optionally comprising light diffusing optics, light focusing optics and/or light filtering optics. Fig. 4 provides a schematic illustration (not to scale) of a printed active matrix LED display on a glass substrate. The display shown comprises 100 pixels and is an approximately 11 inch display. Thin film transistor (TFT) elements, LED elements, gate lines, anode lines and data lines of the device are indicated in Figure 4.

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# **IP BLOCKING POTENTIAL**



USVEISPEERER: CKnowMade