

# Hybrid Bonding

## Patent Landscape Analysis

November 2019

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Key feature of the report		Memory	
Main assignees citing in the report		MEMS	
Objectives of the report		LED	
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# INTRODUCTION

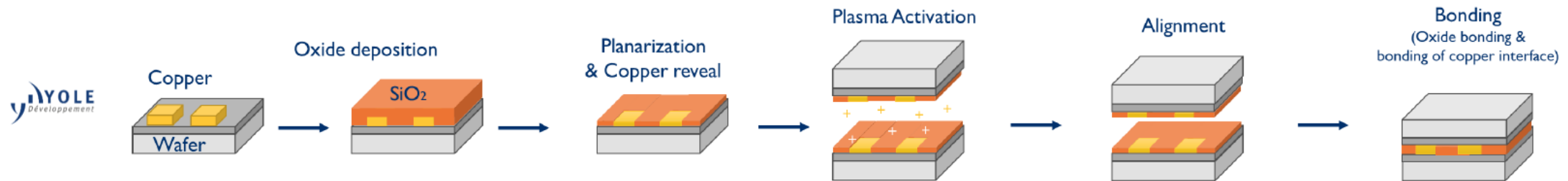
## Hybrid bonding process flow

SAMPLE

For the context of this patent investigation, **hybrid bonding** is defined as a **permanent bond** that combines a **dielectric bond with embedded metal** to form interconnections. It's become known industry-wide as **direct bond interconnect**, or DBI from Xperi/Invensas.

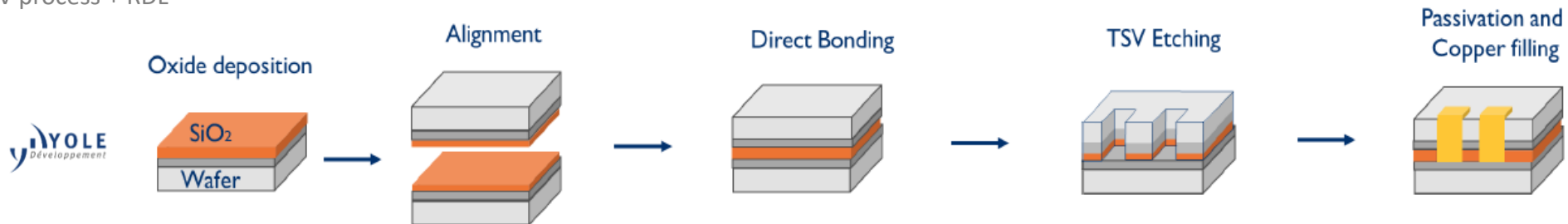
### Hybrid bonding

- Copper pads on both wafers (upper & lower ones)
- Oxide deposition & planarization (very low surface roughness is required ~1nm)
- Plasma activation + alignment (able to go under 1µm of pitch alignment using a stepper) + Room temperature bonding followed by post-alignment and an annealing cycle



### Direct bonding + TSV

- Oxide/oxide permanent bonding
- Via Last TSV process + RDL



# SCOPE OF THE REPORT

- This report provides a detailed picture of the patent landscape for **Hybrid Bonding**.
- This report covers **patents published worldwide** up to **September 2019**. We have selected and analyzed more than **1,005 patents and patent applications** grouped in more than **290 patent families** relevant to the scope of this report.

Example	Selected patents		
	Relevant	Related	Excluded
Patents related to hybrid bonding process	X		
Patents related to devices using hybrid bonding	X		
Patents describing a specific embodiment where hybrid bonding is used		X	
Patents related to ZiBond™ or other dielectric/dielectric bonding			X
Patents related to metal/metal direct bonding			X
Patents related to any bonding process using solder balls, etc.			X

# Understanding the main trends

Who, When and where?

SAMPLE

## IP LANDSCAPE OVERVIEW

Main patent assignee for hybrid bonding technology



## IP LANDSCAPE OVERVIEW

Geographic map of patent applications related to hybrid bonding



Europe

EP Granted		EP Pending	
CEA	9	TSMC	24
TSMC	6	CEA	5
STMICROELECTRONICS	4	SMIC	4
XPERI	2	XPERI	3



South Korea

KR Granted		KR Pending	
TSMC	29	TSMC	19
XPERI	4	SAMSUNG ELECTRONICS	6
SONY	2	SONY	5
CANON	1	XPERI	4
STMICROELECTRONICS	1	CEA	2
CEA	1	CANON	1
	1	RENESAS ELECTRONICS	1
	1	RAYTHEON	1
NATIONAL UNIVERSITY OF SCIENCE AND TECHNOLOGY	1	APPLE	1

Japan



JP Granted

SONY	10
XPERI	7
TSMC	6
CANON	5
CEA	3
PANASONIC	2

JP Pending

SONY	3
XPERI	2
RENESAS ELECTRONICS	2
CANON	1
CEA	1
RAYTHEON	1
QUALCOMM	1
TOSHIBA	1

China

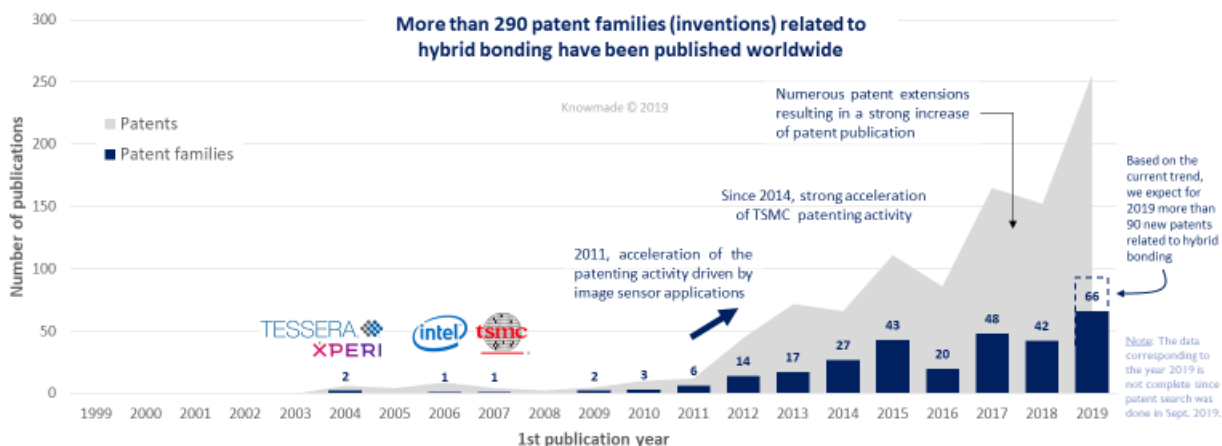


CN Pending

MC	45
ATC	22
NY	6
SHANGHAI IC R & D CENTER	5
MC	4
MNIVISION	4
CHNOLOGIES	4
WON	3

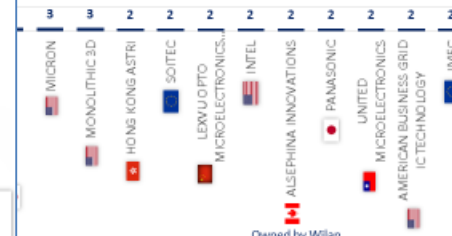
## IP LANDSCAPE OVERVIEW

IP dynamics for hybrid bonding technology



The IP landscape related to hybrid bonding is still small and young. Indeed, despite some early patents published in 2004, the patenting activity really took off in 2010. This growth was first driven by TesserA (now Xperi).

Since 2010, the patenting activity has progressively accelerated but the number of patent families (inventions) remains small. However, the gap between the number of patents (grey color on the graph) and the number of patent family (blue bar graph) is increasing. This trend is the result of the recent development of IP player's patent portfolios which have started to extend their priority patents in foreign countries in order to assert their inventions in all key countries.

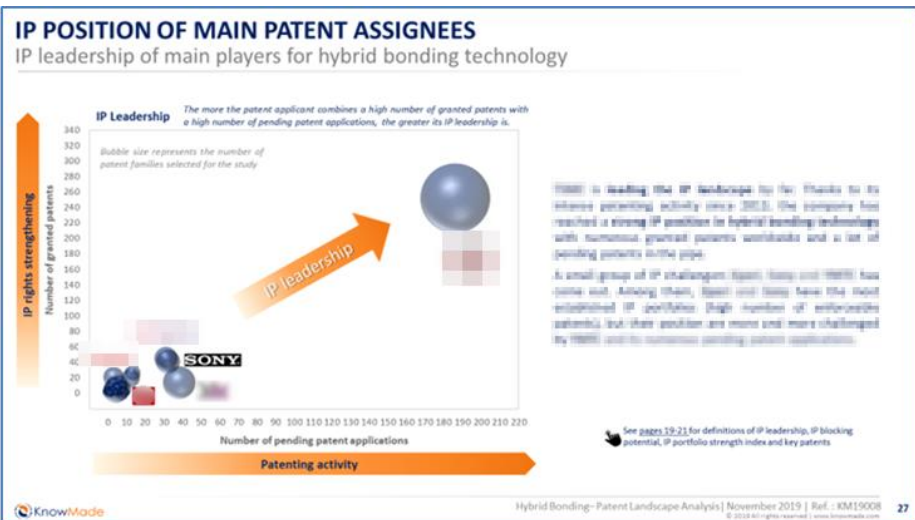


its competitor's. development of memory or image sensor devices. However, the Indeed, today only STMicroelectronics shows a significant Xperi, WiLAN or Intellectual Ventures.

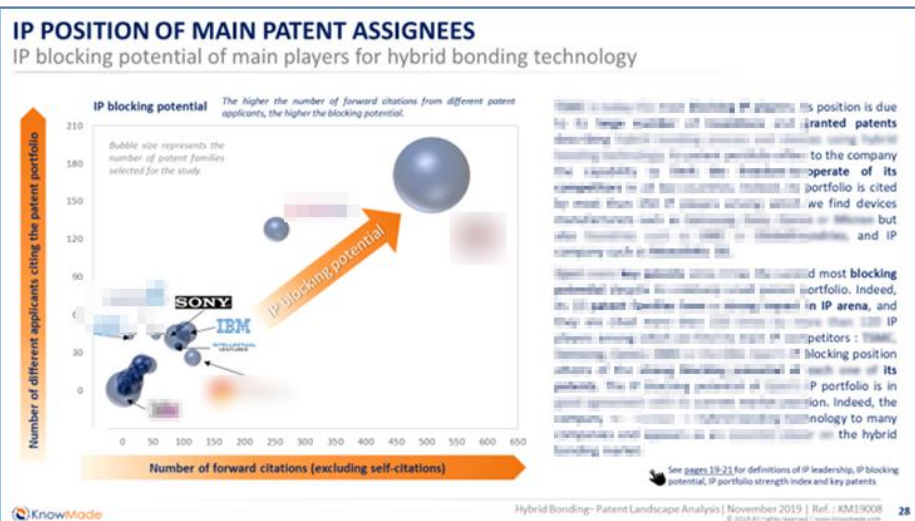
# Comparison of IP players

Portfolios analysis and ranking: who has the strongest IP position and why?

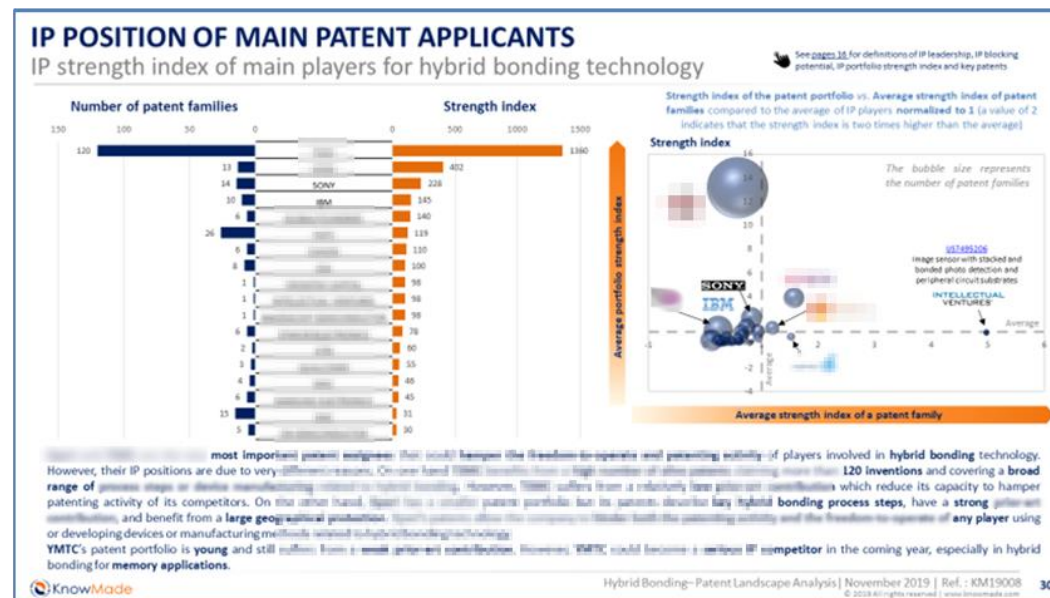
SAMPLE



IP leadership



Blocking potential



# Identify you competitors and their developments

## Corpus segmentation by applications

SAMPLE

### PATENT SEGMENTATION

Main patent assigns by type of patents and applications

	Image sensor	Stacked Memory	Memory on logic	LED	MEMS
	47 inventions	69 inventions	51 inventions	3 inventions	33 inventions

### PATENT SEGMENTATION

Patent claiming device using hybrid bonding technology

**IP Leadership** *The more the patent applicant combines a high number of granted patents with a high number of pending patent applications, the greater its IP leadership is.*

The IP landscape related to devices using hybrid bonding is also... using hybrid bonding is also... competitors.

Indeed, the company seems to have... enforceable patents and... IP coverage thanks to many pending patent applications worldwide.

... However, the company is a... pending related patent landscape and... focused on China.

... is an... almost no recent patenting activity... comes from patent families... that the company is still... technology.

The company portfolio... hybrid bonding process and steps and... manufacturing.

See pages 16 for definitions of IP leadership, IP blocking potential, IP portfolio strength index and key patents

Hybrid Bonding- Patent Landscape Analysis | November 2019 | Ref.: KM19008

### PATENT SEGMENTATION

Time evolution of patent publications by application mentioned in patents

#### Hybrid bonding related patenting activity by application

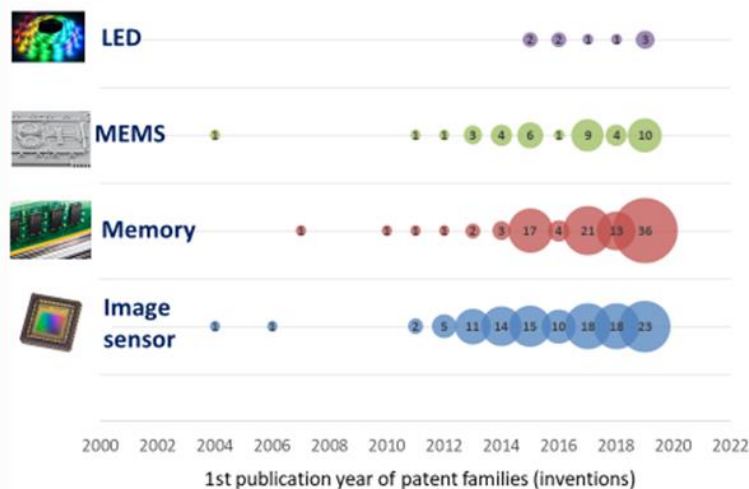


Image sensor and Memory chips are the two first and main applications to use hybrid bonding. Image sensor was the first application to use hybrid bonding, but the patenting acceleration is less important than the case of patents related to memory applications. Today, the number of existing patents related to memory or image sensor applications is quite similar, but the memory manufacturing process seems to be slightly more dynamic.

MEMS is a smaller IP segment that is mainly driven by the patenting activity of TSMC. Indeed, TSMC has started to mention hybrid bonding technology for MEMS in 2011 and has maintained a stable patenting activity since then. However, we can see a small increase of the patenting activity as well as the entry of Xperi in MEMS segment since 2010 opening of the market and potential of using hybrid bonding for MEMS packaging.

MEMS is the first and smallest applications mentioned in the hybrid bonding related patents. Once again, TSMC is the main IP player mentioning MEMS applications.

Even if Xperi's patents mention all these applications, TSMC appears to be the player with the larger IP portfolio in terms of applications and its current patenting activity attests of the company's will to enlarge and strengthen its hybrid bonding portfolio for current and next manufacturing methods but also devices/applications.

# Main IP players portfolio analysis

What are their IP strength, weaknesses, key patents and recent development?

SAMPLE

## KEY PATENTS

TSMC



## IP PROFILE

TSMC : licensing activity



## IP PROFILE

TSMC : patent portfolio overview

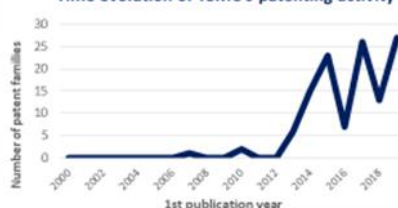
TSMC is the [redacted] of the last 6 years in the hybrid bonding related patent landscape. The company has developed a large patent portfolio that includes hybrid bonding processes and devices using hybrid bonding technology. TSMC benefits from a high [redacted] claiming more than [redacted] inventions and covering a [redacted]. However, the company suffers from a relatively low [redacted] which reduce its capacity to [redacted].

With more than 120 inventions and [redacted] alive patents worldwide, TSMC is today the [redacted] thanks to IP portfolio covering not only key bonding [redacted] but also [redacted] for different applications (CIS, Memory, MEMS, etc.). This [redacted] can be explained by TSMC market position. Indeed, today all the [redacted] bonding solutions for 3D stacking.

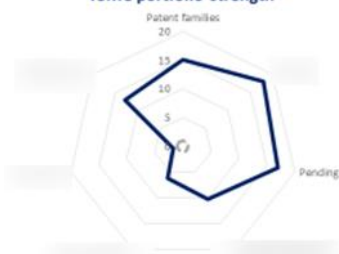
The main difference between [redacted] and TSMC's one is the [redacted] of each one of their patents. Indeed, at a [redacted], each of TSMC's patent has a [redacted].

According to our knowledge, TSMC will probably release its first product (memory) using hybrid bonding process in 2020.

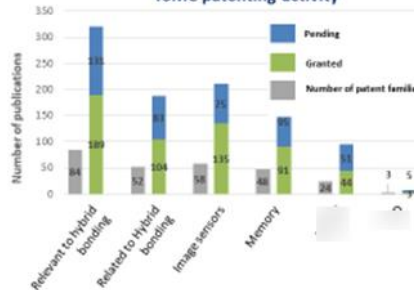
Time evolution of TSMC's patenting activity



TSMC portfolio strength\*



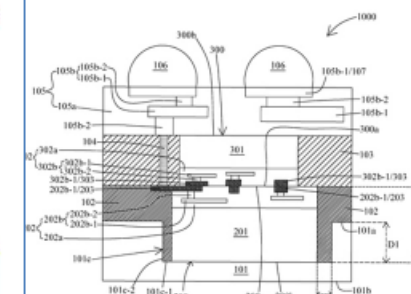
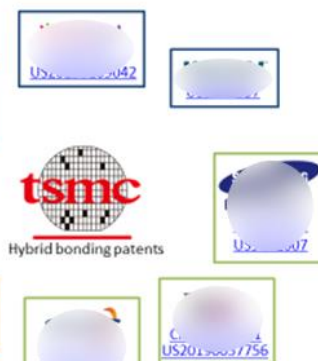
TSMC patenting activity



TSMC alive patent publications per country (without EP and WO)



Main patent assignees citing TSMC's patents



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# Patent database

Excel file containing all the patents analyzed in this report with corpus segmentation

SAMPLE



This Excel database allows multi-criteria searches and includes patent publication number, hyperlinks to the original documents, priority date, title, abstract, patent assignees, legal status and technical segment for each patent families.

Hybrid Bonding - November 2019  
KnowMade

Questel unique family ID (FAN)	Publication numbers	Title	Abstract	Earliest application date	Earliest publication date	Expected expiry dates	Current assignees	Inventors	Legal status (Pending, Granted, Revoked, Expired, Lapsed)	Original document	Hybrid bonding process	Devices using hybrid bonding	Image sensor	Memory	MEMS	LED
86379451	US20190326252	Dbi to si bonding for simplified handle wafer	Devices and techniques without process steps for preparing various microelectronic components for bonding, such as for wafer-to-wafer bonding, wafer-to-wafer bonding, wafer-to-wafer bonding...	2019-04-17	2019-10-24	2039-04-17	INVENSAS BONDING TECHNOLOGIES (US)	CHANDRASEKHAR (US), CHANDRASEKHAR (US)	PENDING	Open						
86379451	WO2019204532	Dbi to si bonding for simplified handle wafer	Devices and techniques without process steps for preparing various microelectronic components for bonding, such as for wafer-to-wafer bonding, wafer-to-wafer bonding, wafer-to-wafer bonding...	2019-04-17	2019-10-24	2021-10-20	INVENSAS BONDING TECHNOLOGIES (US)	CHANDRASEKHAR (US), CHANDRASEKHAR (US)	PENDING	Open						
86304012	US20190319007	Low temperature bonded structures	Methods and techniques for forming interconnect structures to form various microelectronic devices, such as for wafer-to-wafer bonding, wafer-to-wafer bonding, wafer-to-wafer bonding...	2019-03-25	2019-10-17	2039-03-25	INVENSAS BONDING TECHNOLOGIES (US)	LIU JUN	PENDING	Open						
86304012	WO2019199445	Low temperature bonded structures	Methods and techniques for forming interconnect structures to form various microelectronic devices, such as for wafer-to-wafer bonding, wafer-to-wafer bonding, wafer-to-wafer bonding...	2019-03-25	2019-10-17	2021-10-11	INVENSAS BONDING TECHNOLOGIES (US)	LIU JUN	PENDING	Open						
86017594	CN110248427	Three-dimensional memory device having an embedded access memory (DRAM) of a three-dimensional (3D) for	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-04-30	2019-09-17	2039-04-30	YANGTZE MEMORY TECHNOLOGIES (CN)	LIU JUN	PENDING	Open	X			X		
85913273	US20190273109	Method for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-06-21	2019-09-03	2039-06-21	SONY (JP)	LIU JUN	PENDING	Open		X	X			
85894425	US20190273108	Solid state imaging device and method for manufacturing the same	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-06-21	2019-09-03	2039-06-21	RENEAS ELECTRONICS (JP)	LIU JUN	PENDING	Open	X	X	X			
85894425	JP2019153675	Solid state imaging device and method for manufacturing the same	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-06-21	2019-09-03	2039-06-21	RENEAS ELECTRONICS	LIU JUN	PENDING	Open	X	X	X			
85851827	CN110192269	Three-dimensional memory device having an embedded access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-06-21	2019-09-03	2039-06-21	YANGTZE MEMORY TECHNOLOGIES (CN)	LIU JUN	PENDING	Open				X		
85834472	US10402577	Method for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-09-03	2019-09-03	2039-09-03	INVENSAS	LIU JUN	GRANTED	Open						
85834472	US20190341350	Method for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2019-09-03	2019-09-03	2039-09-03	INVENSAS (US)	LIU JUN	PENDING	Open						
85827647	US20190268555	CMOS image sensor having a divided bit line	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-12-17	2019-08-29	2039-12-17	OMNIVISION TECHNOLOGIES (US)	LIU JUN	PENDING	Open	X		X			
85827647	CN110191295	CMOS image sensor having a divided bit line	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-12-17	2019-08-29	2039-12-17	OMNIVISION TECHNOLOGIES (US)	LIU JUN	PENDING	Open	X		X			
85827645	US20190268556	CMOS image sensor having a divided bit line	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-12-17	2019-08-29	2039-12-17	OMNIVISION TECHNOLOGIES (US)	LIU JUN	PENDING	Open	X		X			
85827645	CN110191296	CMOS image sensor having a divided bit line	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-12-17	2019-08-29	2039-12-17	OMNIVISION TECHNOLOGIES (US)	LIU JUN	PENDING	Open	X		X			
85827645	TW201937716	CMOS image sensor having a divided bit line	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-12-17	2019-08-29	2039-12-17	OMNIVISION TECHNOLOGIES (US)	LIU JUN	PENDING	Open	X		X			
85862145	US20190252375	Back biasing of FD-SOI circuit blocks	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-02-09	2019-08-15	2038-02-09	XCELSIS	LIU JUN	PENDING	Open	X		X		X	
85862139	US20190252353	Face-to-face three-dimensional integrated circuit of a three-dimensional (3D) for access memory (DRAM)...	Devices and techniques for forming a three-dimensional (3D) for access memory (DRAM) of a three-dimensional (3D) for access memory (DRAM)...	2018-02-14	2019-08-15	2039-02-06	ENERGIE ATOMIQUE & AUX ENERGIES ALTERNATIVES (FR)	LIU JUN	PENDING	Open	X			X		

**Patent information**  
Dates and numbers of priority/application/publication/grant  
Title, abstract, claims  
Patent applicants, current assignees, inventors  
Current legal status of patents (granted, pending, expired, etc.)

**Segments**  
Hybrid bonding process, Device using hybrid bonding, Image sensor, Memory, MEMS, LED, etc.

# ORDER FORM

## Hybrid bonding

Patent Landscape Analysis – November 2019

Ref.:KM19008



### SHIP TO

Name (Mr/Ms/Dr/Pr):

Job Title:

Company:

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City:

State:

Postcode/Zip:

Country:

VAT ID Number for EU members:

Tel:

Email:

Date:

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06902 Valbonne Sophia Antipolis  
FRANCE

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Bank: Banque Populaire Méditerranée, CAP 3000 Quartier du lac, 06700 St Laurent du Var, France  
IBAN: FR76 1460 7003 6360 6214 5695 139  
BIC/SWIFT: CCBPFRPPMAR

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1.3 Orders are deemed to be accepted only upon written acceptance and confirmation by the Seller, within [7 days] from the date of order, to be sent either by email or to the Buyer’s address. In the absence of any confirmation in writing, orders shall be deemed to have been accepted.

## 2. MAILING OF THE PRODUCTS

2.1 Products are sent by email to the Buyer:

- within [1] month from the order for Products already released; or

- within a reasonable time for Products ordered prior to their effective release. In this case, the Seller shall use its best endeavours to inform the Buyer of an indicative release date and the evolution of the work in progress.

2.2 Some weeks prior to the release date the Seller can propose a pre-release discount to the Buyer.

The Seller shall by no means be responsible for any delay in respect of article 2.2 above, and including in cases where a new event or access to new contradictory information would require for the analyst extra time to compute or compare the data in order to enable the Seller to deliver a high quality Products.

2.3 The mailing of the Product will occur only upon payment by the Buyer, in accordance with the conditions contained in article 3.

2.4 The mailing is operated through electronic means either by email via the sales department. If the Product’s electronic delivery format is defective, the Seller undertakes to replace it at no charge to the Buyer provided that it is informed of the defective formatting within 90 days from the date of the original download or receipt of the Product.

2.5 The person receiving the Products on behalf of the Buyer shall immediately verify the quality of the Products and their conformity to the order. Any claim for apparent defects or for non-conformity shall be

sent in writing to the Seller within 8 days of receipt of the Products. For this purpose, the Buyer agrees to produce sufficient evidence of such defects.

2.6 No return of Products shall be accepted without prior information to the Seller, even in case of delayed delivery. Any Product returned to the Seller without providing prior information to the Seller as required under article 2.5 shall remain at the Buyer’s risk.

## 3. PRICE, INVOICING AND PAYMENT

3.1 Prices are given in the orders corresponding to each Product sold on a unit basis or corresponding to annual subscriptions. They are expressed to be inclusive of all taxes. The prices may be reevaluated from time to time. The effective price is deemed to be the one applicable at the time of the order.

3.2 Payments due by the Buyer shall be sent by cheque payable to Knowmade, PayPal or by electronic transfer to the following account:

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BIC or SWIFT code: CCBPFRPPMAR

IBAN: : FR76 1460 7003 6360 6214 5695 139

To ensure the payments, the Seller reserves the right to request down payments from the Buyer. In this case, the need of down payments will be mentioned on the order.

3.3 Payment is due by the Buyer to the Seller within 30 days from invoice date, except in the case of a particular written agreement. If the Buyer fails to pay within this time and fails to contact the Seller, the latter shall be entitled to invoice interest in arrears based on the annual rate Refi of the «BCE» + 7 points, in accordance with article L. 441-6 of the French Commercial Code. Our publications (report, database, tool...) are delivered only after reception of the payment.

3.4 In the event of termination of the contract, or of misconduct, during the contract, the Seller will have the right to invoice at the stage in progress, and to take legal action for damages.

## 4. LIABILITIES

4.1 The Buyer or any other individual or legal person acting on its behalf, being a business user buying the Products for its business activities, shall be solely responsible for choosing the Products and for the use and interpretations he makes of the documents it purchases, of the results he obtains, and of the advice and acts it deduces thereof.

4.2 The Seller shall only be liable for (i) direct and (ii) foreseeable pecuniary loss, caused by the Products or arising from a material breach of this agreement

4.3 In no event shall the Seller be liable for:

a) damages of any kind, including without limitation, incidental or consequential damages (including, but not limited to, damages for loss of profits, business interruption and loss of programs or information) arising out of the use of or inability to use the Seller’s website or the Products, or any information provided on the website, or in the Products;

b) any claim attributable to errors, omissions or other inaccuracies in the Product or interpretations thereof.

4.4 All the information contained in the Products has been obtained from sources believed to be reliable. The Seller does not warrant the accuracy, completeness adequacy or reliability of such information, which cannot be guaranteed to be free from errors.

4.5 All the Products that the Seller sells may, upon prior notice to the Buyer from time to time be modified by or substituted with similar Products meeting the needs of the Buyer. This modification shall not lead to the liability of the Seller, provided that the Seller ensures the substituted Product is similar to the Product initially ordered.

4.6 In the case where, after inspection, it is acknowledged that the Products contain defects, the Seller undertakes to replace the defective products as far as the supplies allow and without indemnities or compensation of any kind for labor costs, delays, loss caused or any other reason. The replacement is guaranteed for a maximum of two months starting from the delivery date. Any replacement is excluded for any event as set out in article 5 below.

4.7 The deadlines that the Seller is asked to state for the mailing of the Products are given for information only and are not guaranteed. If such deadlines are not met, it shall not lead to any damages or cancellation of the orders, except for non-acceptable delays exceeding [4] months from the stated deadline, without information from the Seller. In such case only, the Buyer shall be entitled to ask for a reimbursement of its first down payment to the exclusion of any further damages.

4.8 The Seller does not make any warranties, express or implied, including, without limitation, those of

saleability and fitness for a particular purpose, with respect to the Products. Although the Seller shall take reasonable steps to screen Products for infection of viruses, worms, Trojan horses or other codes containing contaminating or destructive properties before making the Products available, the Seller cannot guarantee that any Product will be free from infection.

## 5. FORCE MAJEURE

The Seller shall not be liable for any delay in performance directly or indirectly caused by or resulting from acts of nature, fire, flood, accident, riot, war, government intervention, embargoes, strikes, labor difficulties, equipment failure, late deliveries by suppliers or other difficulties which are beyond the control, and not the fault of the Seller.

## 6. PROTECTION OF THE SELLER’S IPR

6.1 All the IPR attached to the Products are and remain the property of the Seller and are protected under French and international copyright law and conventions.

6.2 The Buyer agreed not to disclose, copy, reproduce, redistribute, resell or publish the Product, or any part of it to any other party other than employees of its company. The Buyer shall have the right to use the Products solely for its own internal information purposes. In particular, the Buyer shall therefore not use the Product for purposes such as:

- Information storage and retrieval systems;

- Recordings and re-transmittals over any network (including any local area network);

- use in any timesharing, service bureau, bulletin board or similar arrangement or public display;

- Posting any Product to any other online service (including bulletin boards or the Internet);

- Licensing, leasing, selling, offering for sale or assigning the Product.

6.3 The Buyer shall be solely responsible towards the Seller of all infringements of this obligation, whether this infringement comes from its employees or any person to whom the Buyer has sent the Products and shall personally take care of any related proceedings, and the Buyer shall bear related financial consequences in their entirety.

6.4 The Buyer shall define within its company point of contact for the needs of the contract. This person will be the recipient of each new report in PDF format. This person shall also be responsible for respect of the copyrights and will guaranty that the Products are not disseminated out of the company.

## 7. TERMINATION

7.1 If the Buyer cancels the order in whole or in part or postpones the date of mailing, the Buyer shall indemnify the Seller for the entire costs that have been incurred as at the date of notification by the Buyer of such delay or cancellation. This may also apply for any other direct or indirect consequential loss that may be borne by the Seller, following this decision.

7.2 In the event of breach by one Party under these conditions or the order, the non-breaching Party may send a notification to the other by recorded delivery letter upon which, after a period of thirty (30) days without solving the problem, the non-breaching Party shall be entitled to terminate all the pending orders, without being liable for any compensation.

## 8. MISCELLANEOUS

All the provisions of these Terms and Conditions are for the benefit of the Seller itself, but also for its licensors, employees and agents. Each of them is entitled to assert and enforce those provisions against the Buyer.

Any notices under these Terms and Conditions shall be given in writing. They shall be effective upon receipt by the other Party.

The Seller may, from time to time, update these Terms and Conditions and the Buyer, is deemed to have accepted the latest version of these terms and conditions, provided they have been communicated to him in due time.

## 9. GOVERNING LAW AND JURISDICTION

9.1 Any dispute arising out or linked to these Terms and Conditions or to any contract (orders) entered into in application of these Terms and Conditions shall be settled by the French Commercial Courts of Grasse, which shall have exclusive jurisdiction upon such issues.

9.2 French law shall govern the relation between the Buyer and the Seller, in accordance with these Terms and Conditions.



# Knowmade Company

# WHAT WE DO

Knowmade helps customers to understand the **competitive landscape**, follow **technology trends**, and find out **opportunities** and **threats** in terms of **technology** and **patents**.

- Interpreting the **competitive landscape** and **technology developments** throughout **patents** and **scientific information**.
- Turning **patents** and **scientific information** into **business intelligence tools** that give you the capability to
  - Understand your **competitive environment**
  - Be ahead of **technology trends**
  - Identify patent & technology **opportunities**
  - Assess patent & technology **risks**
  - Define your **IP** and **R&D strategy**
  - Monetize your **technologies** and know-how
  - Defend your **business**
- Strong **technology expertise** with an in-depth **knowledge of patents** and **scientific information**.
- Highly **specialized** analysts in the following sectors:

## Electronics, Photonics and Wireless communications

Compound semiconductors, Power electronics, Batteries, Memories, RF devices & technologies, Wireless communications, Solid-state lighting & display, Photonics, MEMS, Sensors and Actuators, Semiconductor manufacturing and Advanced packaging.

## Life Sciences, Healthcare and Agri-Food

Medical devices, Medical imaging, Microfluidics, Biotechnology, Pharmaceuticals, Food-processing



Patents  
Technologies  
Prior art  
Scientific findings  
Opportunities  
Partners  
Competitors  
Newcomers  
M&A targets



Patent landscape analysis  
Scientific review  
IP portfolio assessment  
Patent valuation  
Freedom-to-operate analysis  
Litigation & licensing support  
Patents linked to products  
Technology scouting  
Technology trends  
Competitive IP landscape  
Market trends  
Reverse engineering

Make strategic decisions  
Sustain competitive advantages  
Speed R&D and enhance innovation process  
Align R&D and IP with key business objectives  
Strengthen IP portfolio and acquire technologies  
Anticipate risks and defend core businesses  
Explore new opportunities and monetize IP

# OUR ADDED VALUE

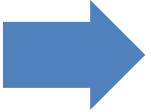
## Patent Search

- ✓ **Strong technical expertise of our analysts with PhD degree**
  - Comprehensive search queries and keywords
  - Manual selection of relevant and related patents
  - Manual segmentation by technology & application



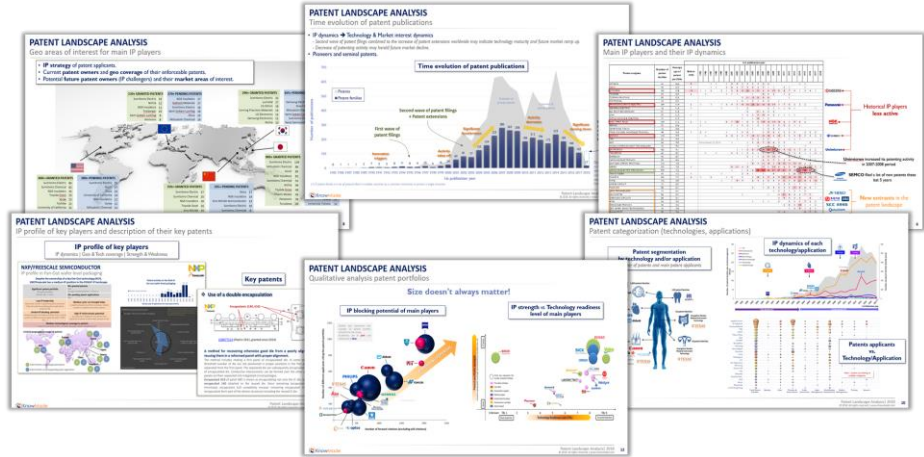
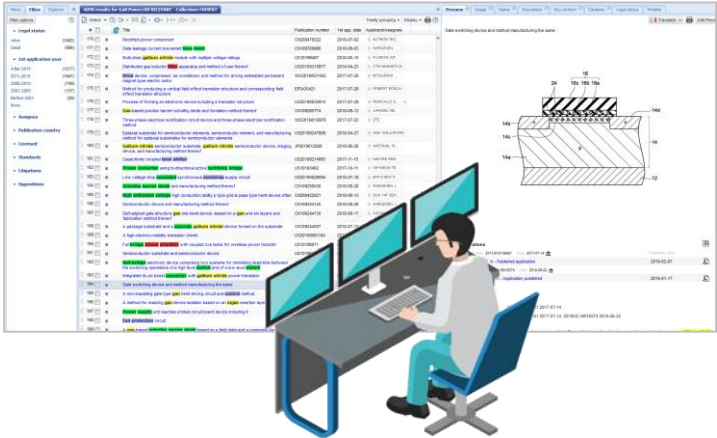
## Analytics

- ✓ **State of the art statistical tools**
- ✓ **Innovative methodologies to deliver relevant IP analysis**
- ✓ **Business oriented data representation and graphics**



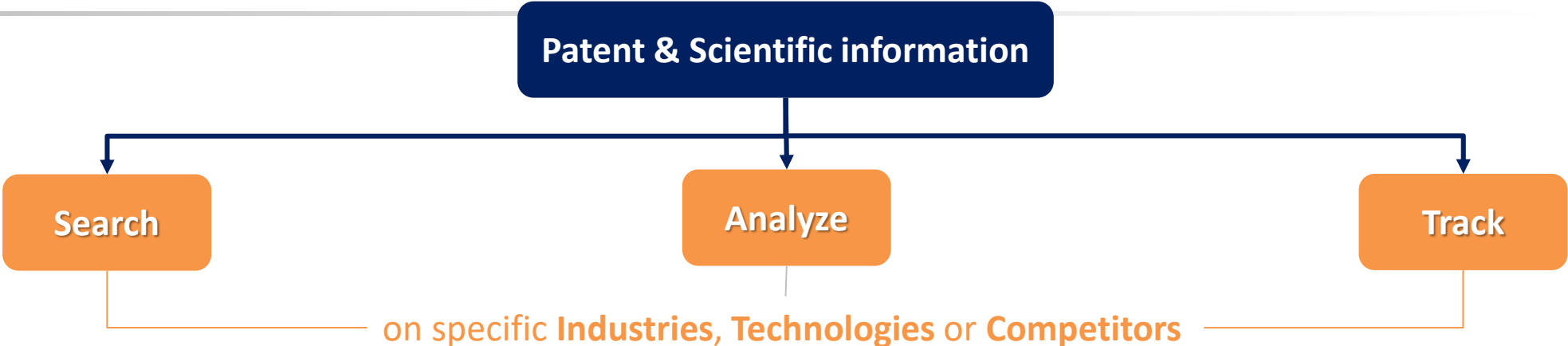
## Results Analysis

- ✓ **Technical expertise**
  - Highly specialized analysts in your field
  - Benefit from knowledge capitalization
- ✓ **In-depth IP analysis combined with market data and reverse engineering \***
- ✓ **Customer support**



\* Our partners

# KNOWMADE ACTIVITIES



### Prior-art search

*Is my invention novel?*

### Technology scouting

*Are there patents or technologies to acquire? ... that could be drawn on to improve R&D?*

### Patent landscape analysis

*Competitive & technology landscape analysis through patents:  
Who? What? Where? Since when? With who?*

### Freedom-to-operate

*Am I free to sell my product without infringing third-parties IP rights?*

### Evidence of use (litigation/licensing)

*Make the link between patents and product features*

### Patent valuation

*What are the most valuable patents and what is their financial value?*

### Patent monitoring service

*Monitor the IP activity: new applications, new granted patents, patents newly expired*

### Scientific watch

*Monitor the Scientific activity*

### Competitor watch

*Monitor the competitors R&D activities*



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